

FIG.8

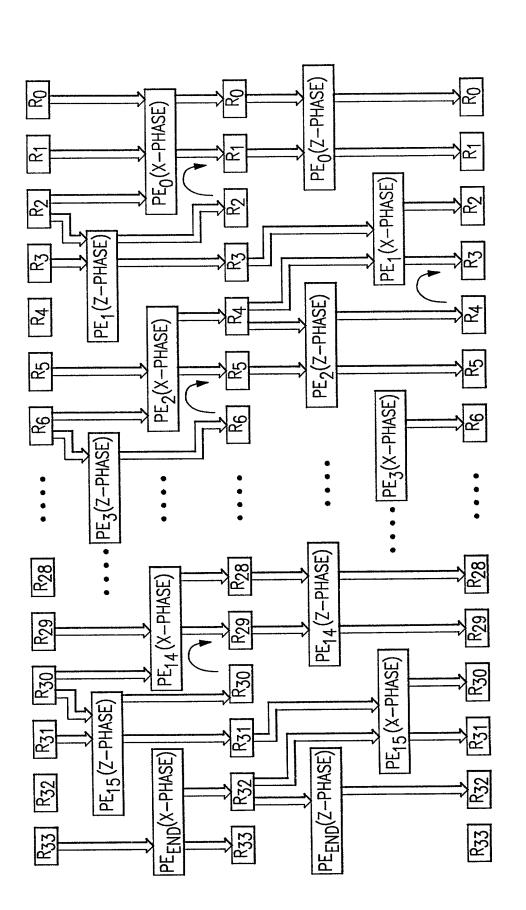
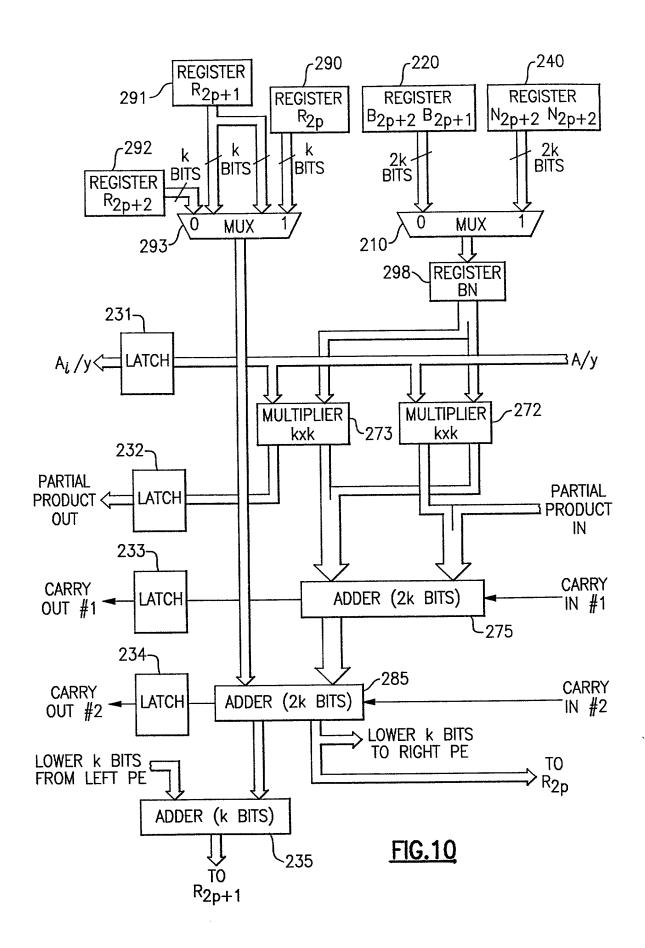


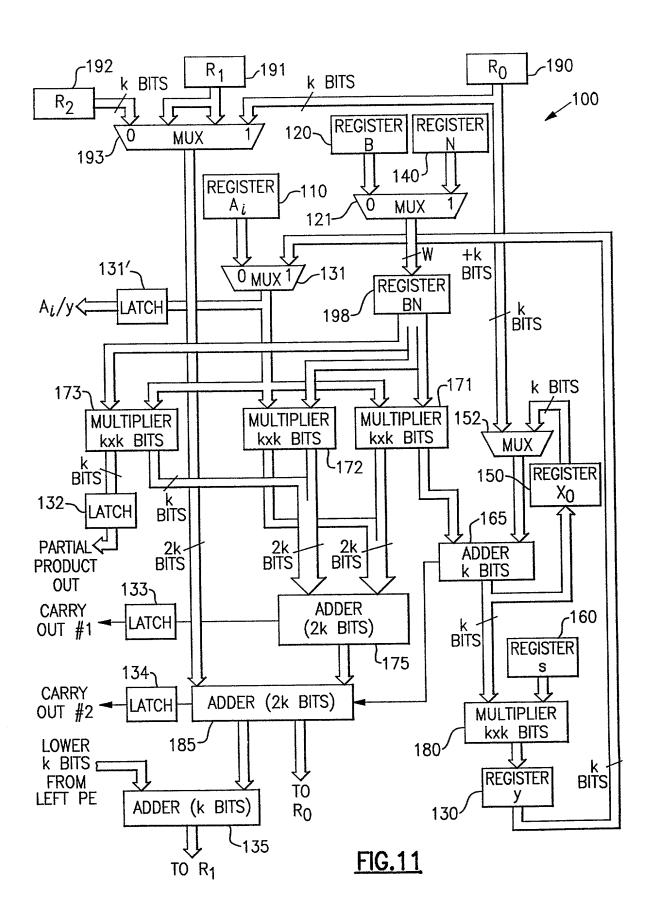
FIG.9

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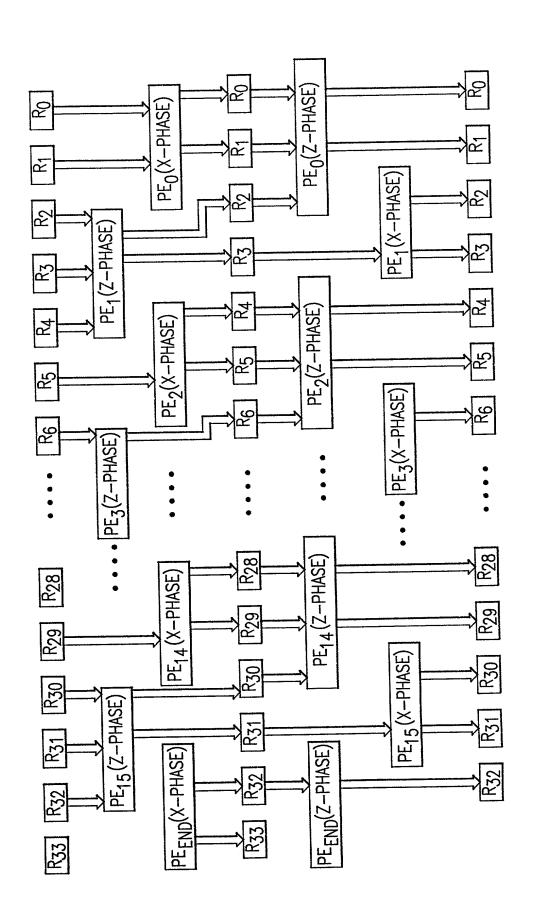
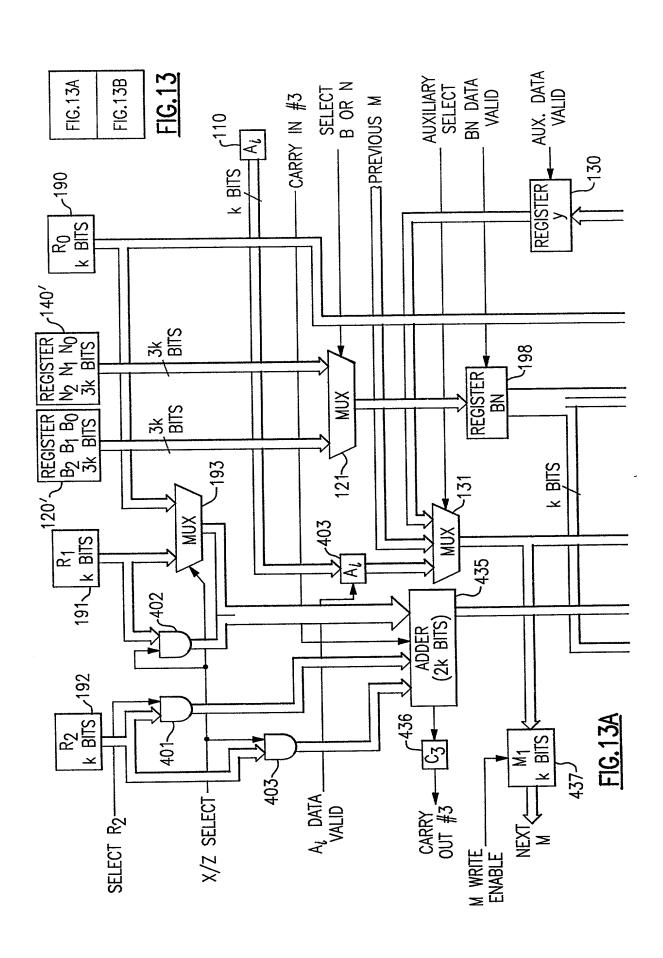
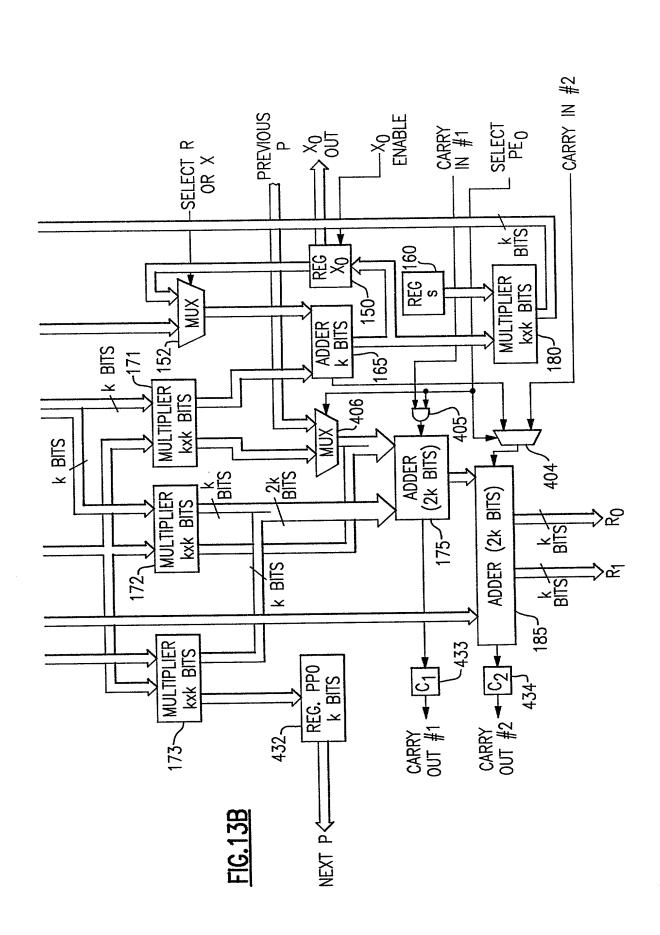


FIG. 12





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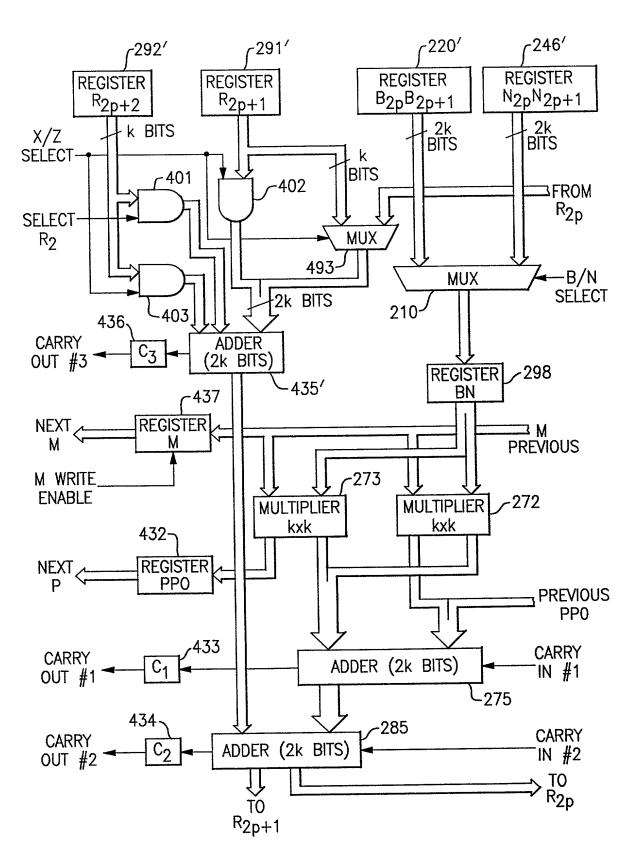


FIG.14

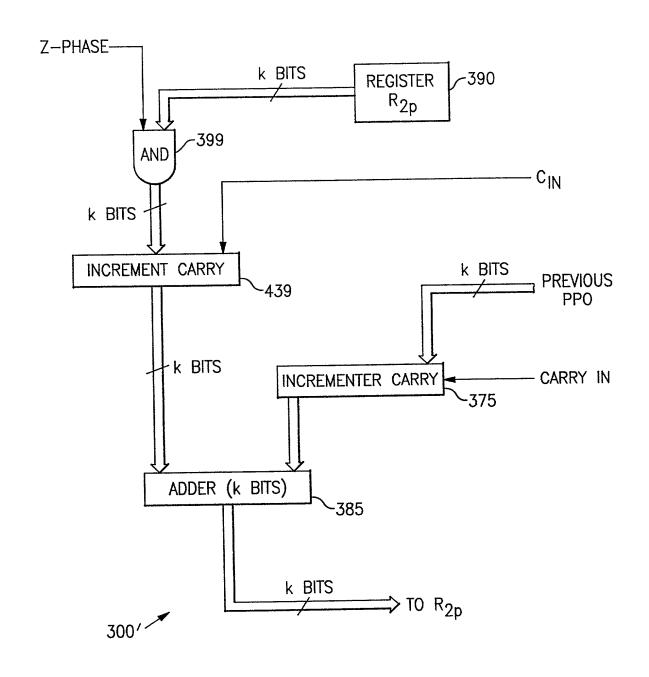


FIG.15

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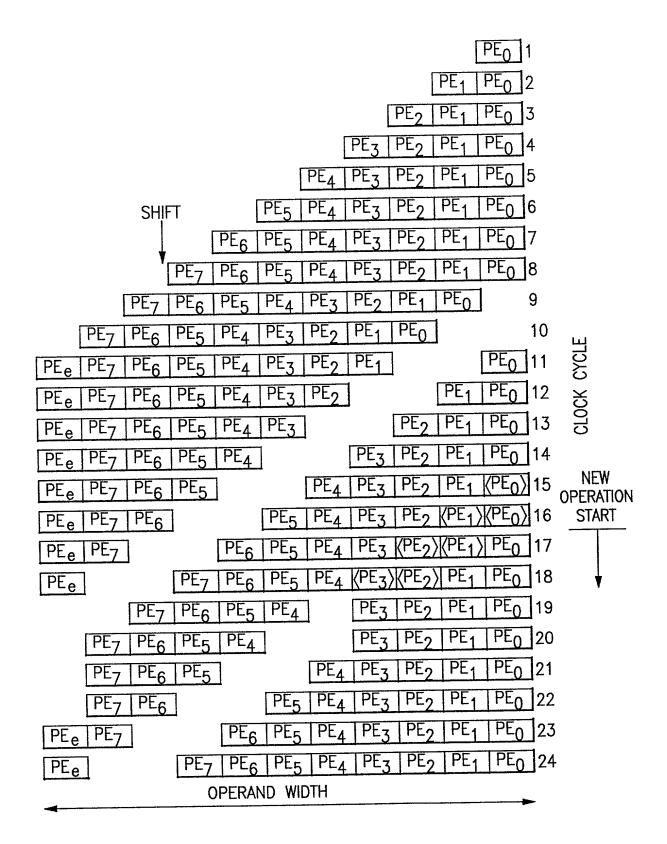


FIG.16

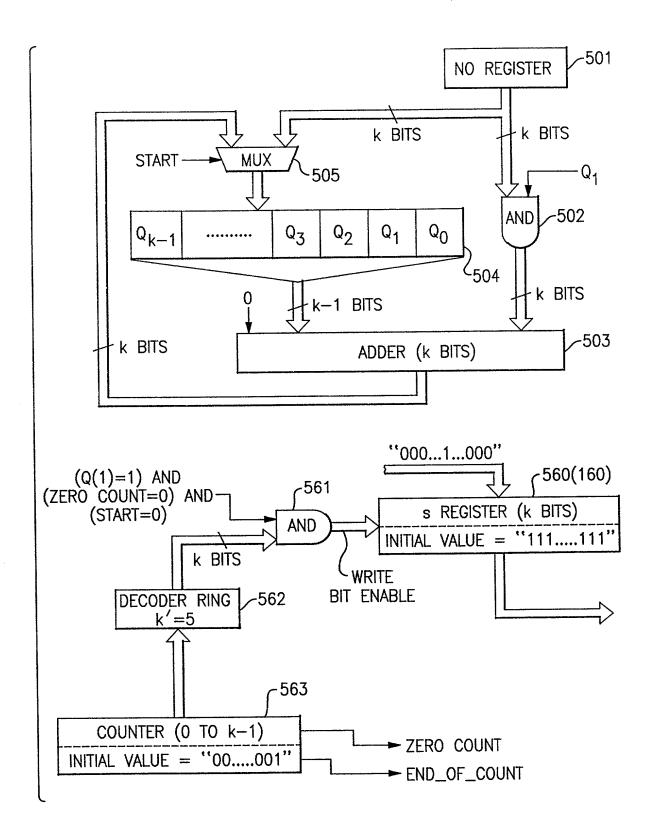
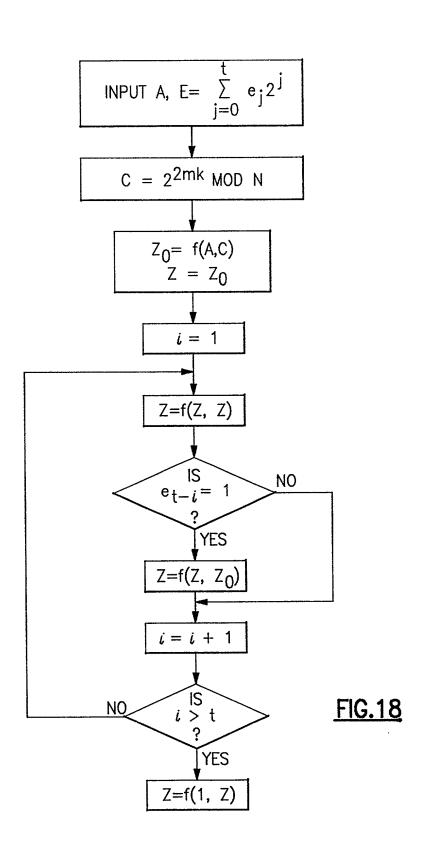
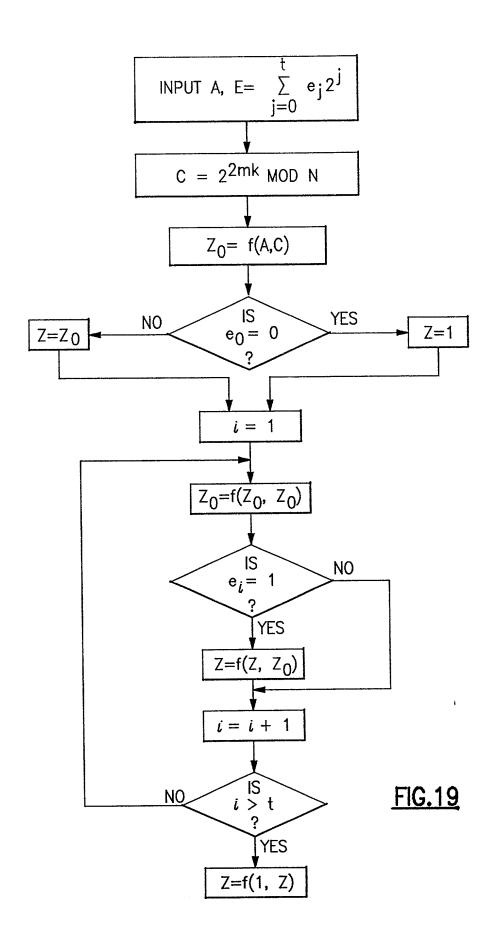


FIG.17





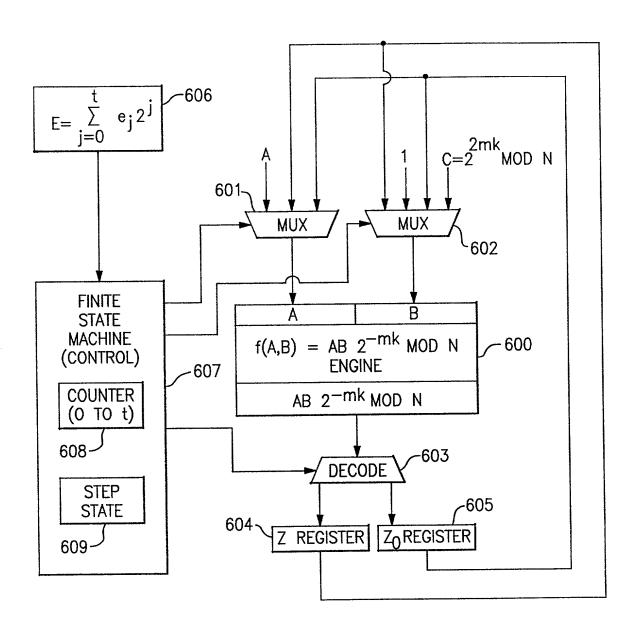
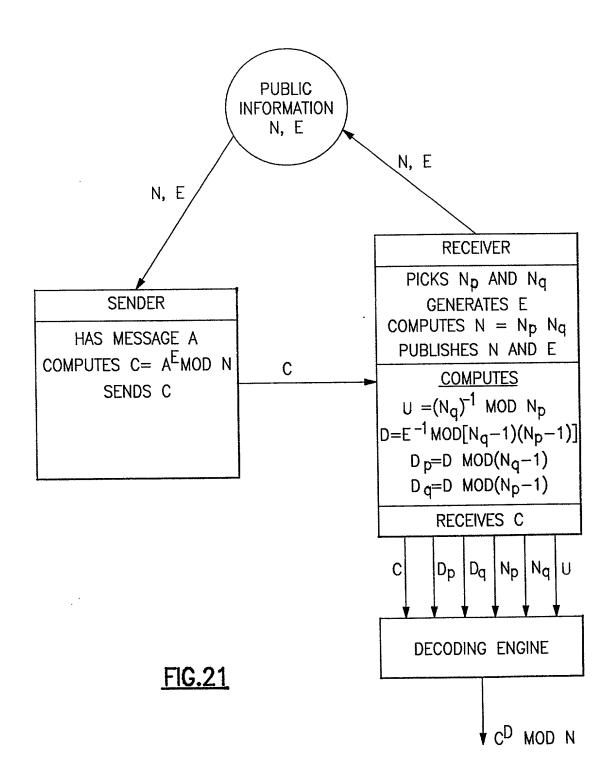


FIG.20



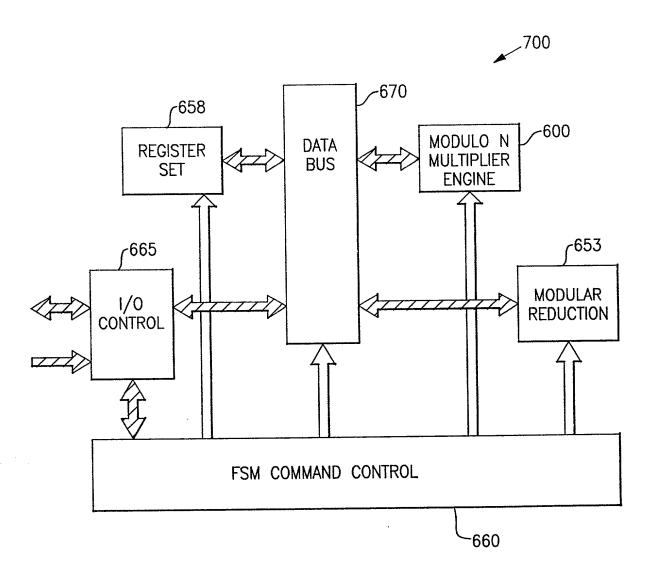
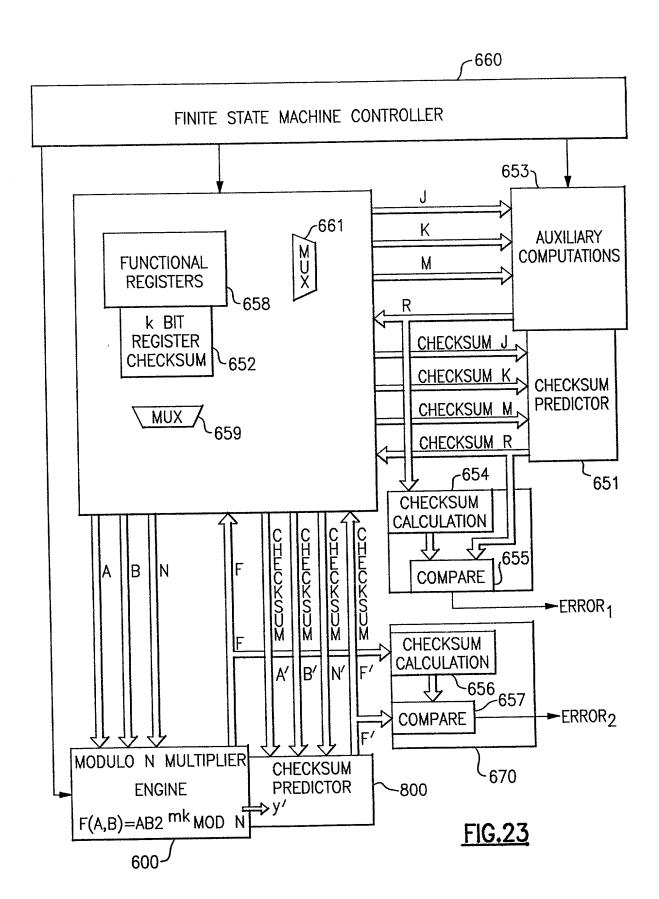


FIG.22

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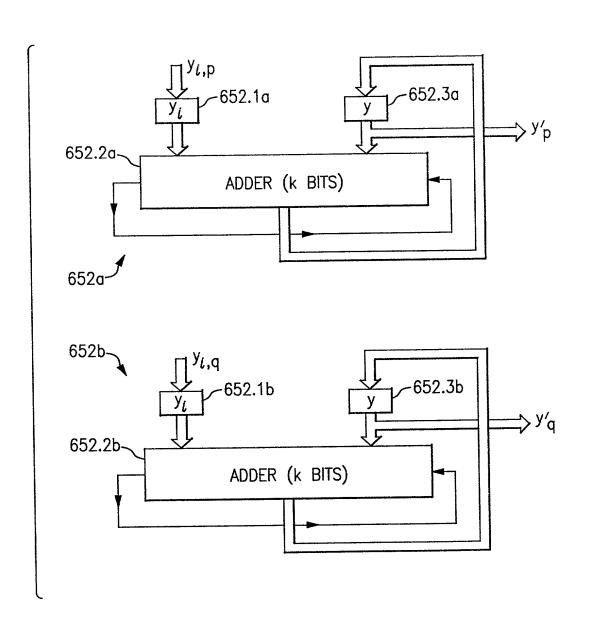


FIG.24

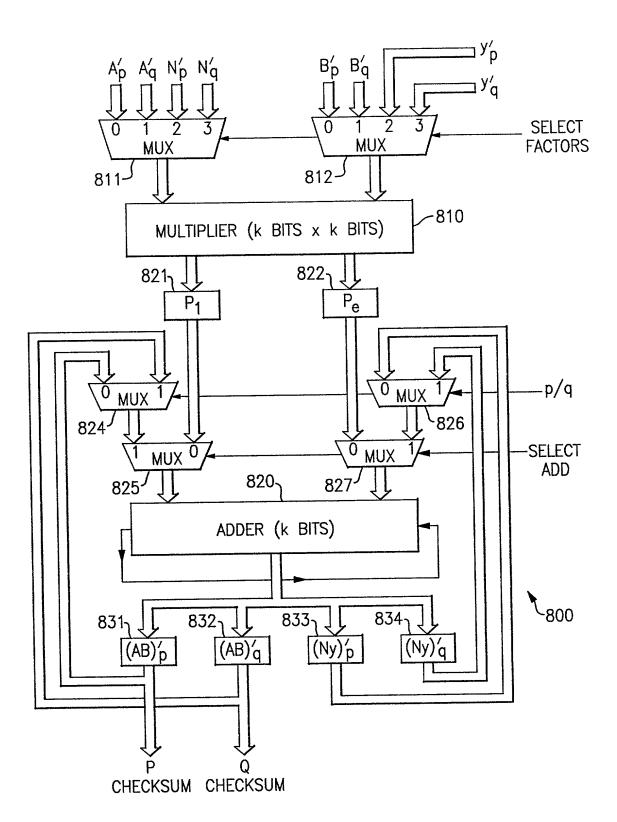
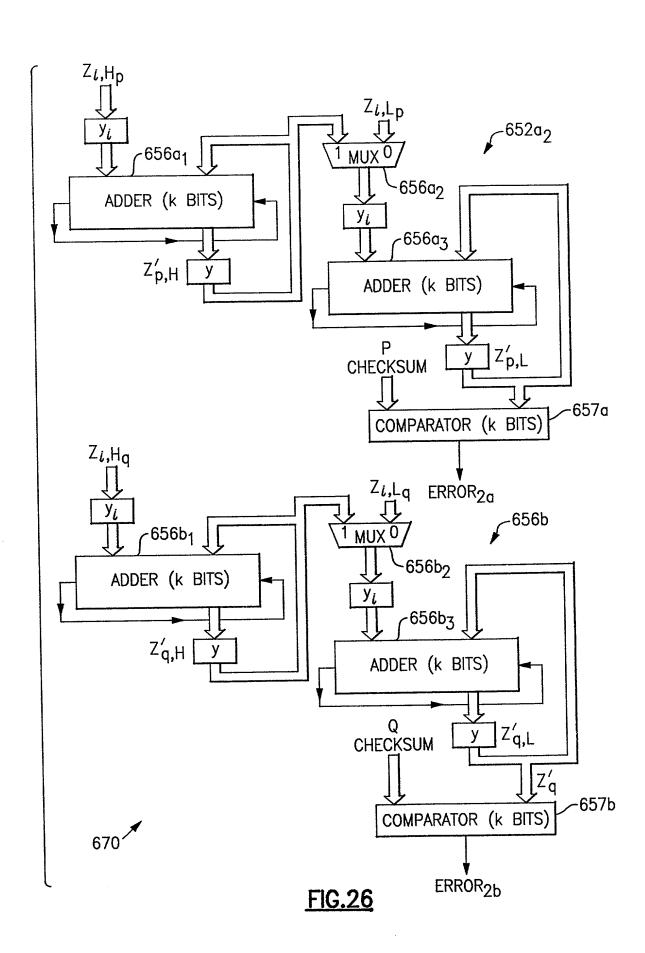


FIG.25

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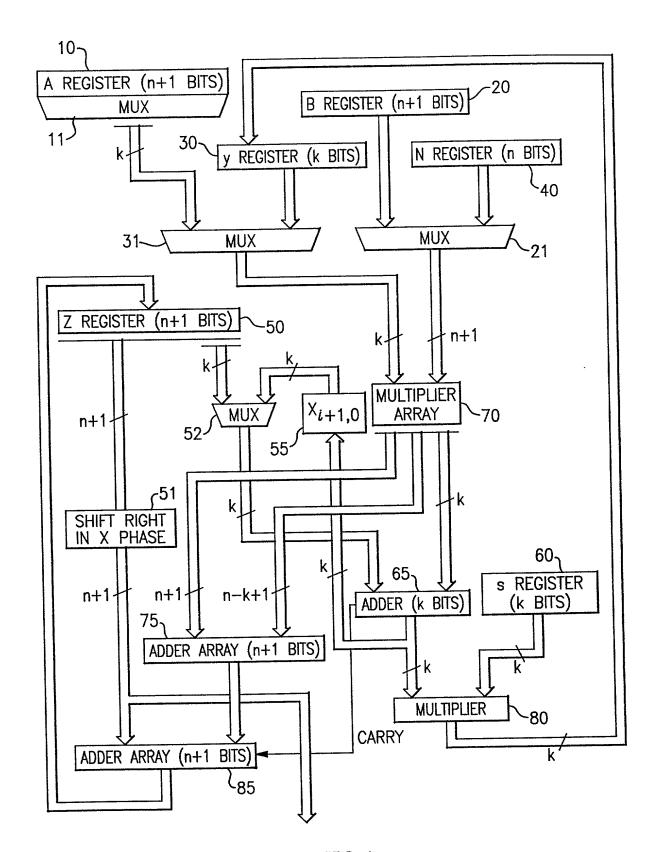


FIG.1

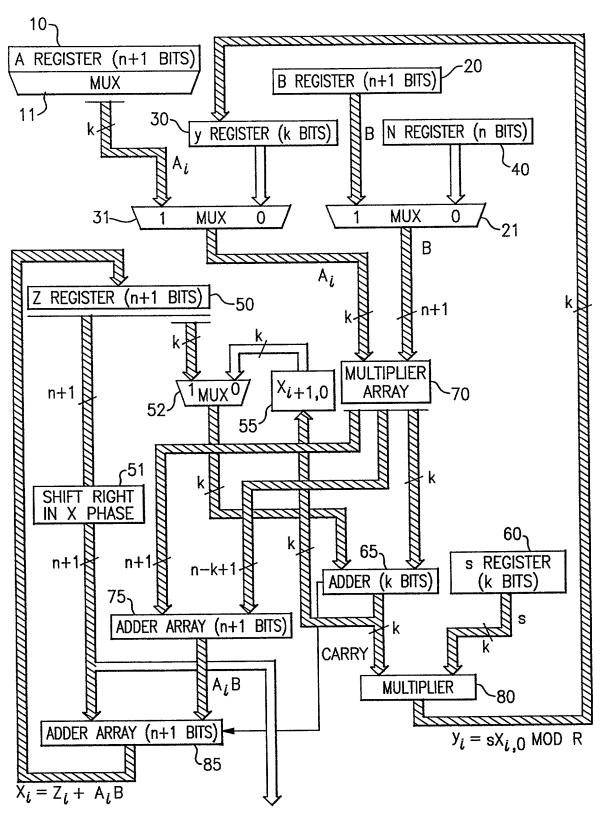


FIG.2

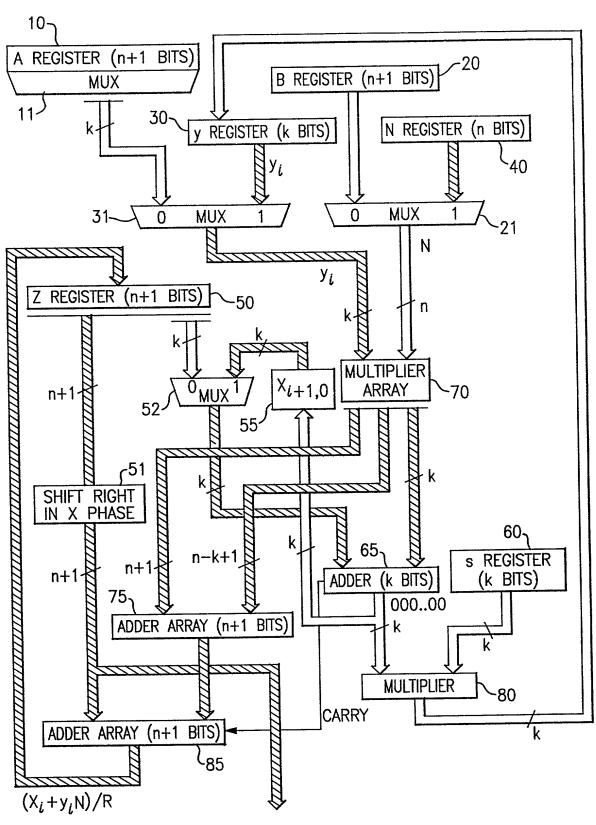
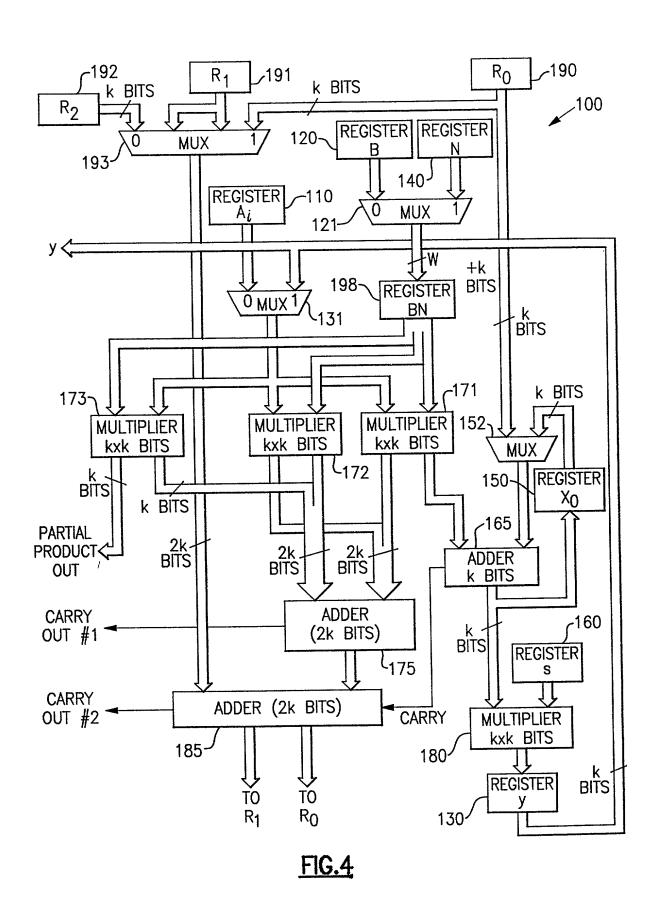


FIG.3



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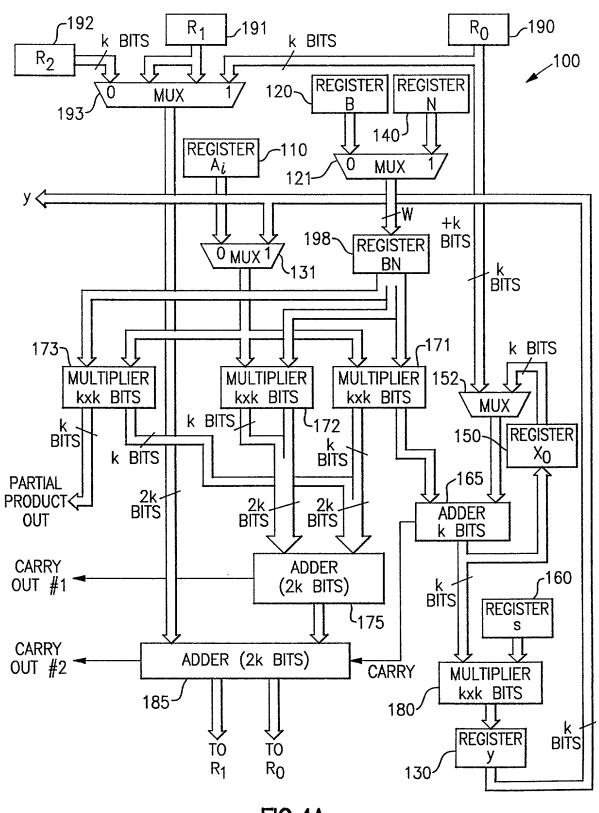
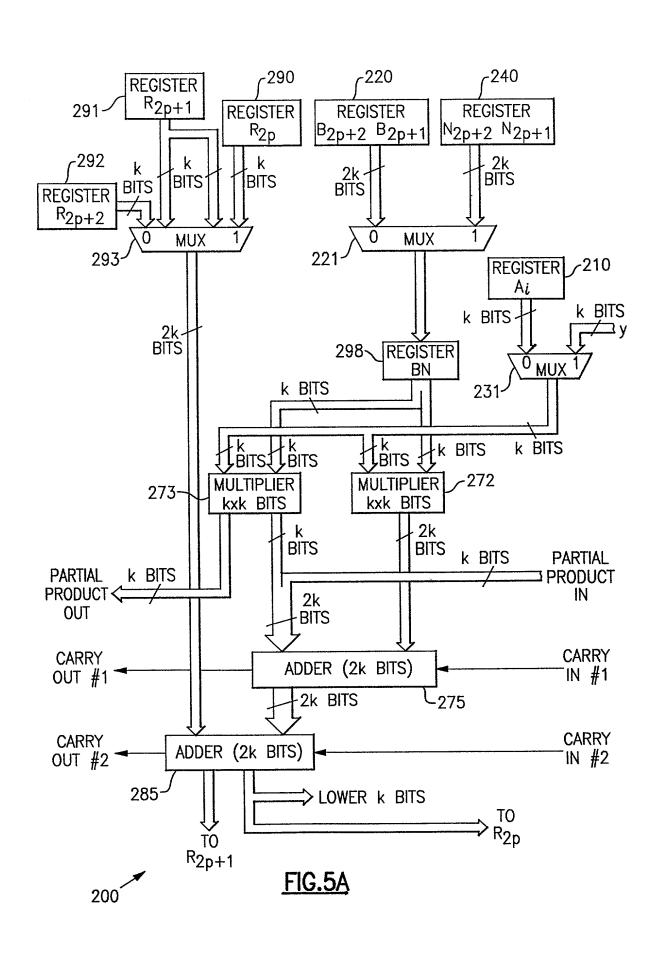


FIG.4A



c240 ₇290 _c220 REGISTER R_{2p+1} REGISTER 291-REGISTER **REGISTER** $B_{2p+2} B_{2p+1}$ $N_{2p+2} N_{2p+1}$ R_{2p} -292 k 2k BITS 2k · BITS BITS BITS BITS **REGISTER** R_{2p+2} MUX MUX 293 221 REGISTER /210 Αį k BITS k BITS 2k-BITS REGISTER BN 298~ MUX k BITS 231 k BITS k BITS k BITS BITS k BITS MULTIPLIER __272 kxk BITS MULTIPLIER kxk BITS 273 k BITS **BITS** k BITS **PARTIAL** k BITS PARTIAL 3 PRODUCT PRODUCT < 2k IN 2k OUT **BITS BITS CARRY CARRY** ADDER (2k BITS) IN #1 OUT #1 -2k BITS -275 **CARRY CARRY** ADDER (2k BITS) IN #2 OUT #2 285 > LOWER k BITS TO R_{2p} TO R_{2p+1} FIG.5 200



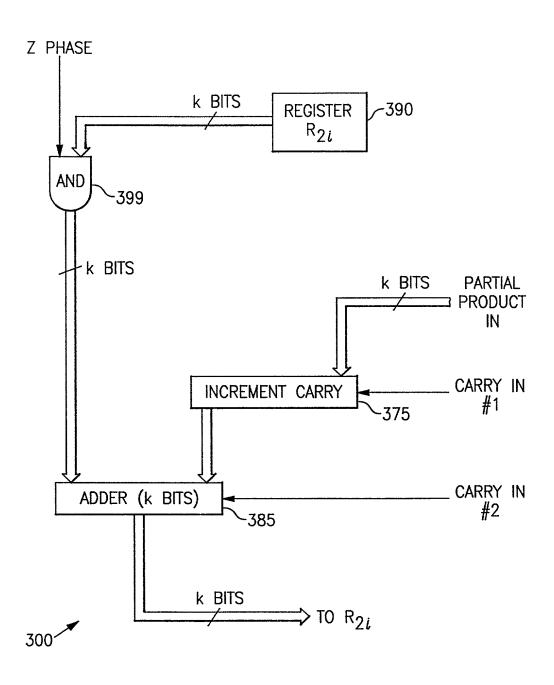


FIG.6

